

SKiiP 2414 GB12E4-4DUL



SKiiP® 4

2-pack-integrated intelligent Power System

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Features

- Intelligent Power Module
- Integrated current and temperature measurement
- Integrated DC-link measurement
- Solder free power section
- IGBT4 and CAL4F technology
- $T_{jmax} = 175^{\circ}\text{C}$
- Safety isolated switching and sensor signals
- Digital signal transmission
- 100% tested IPM
- RoHS compliant
- UL recognition in progress, file no. E242581

Typical Applications*

- Renewable energies
- Traction
- Elevators
- Industrial drives

Remarks

For further information please refer to SKiiP®4 Technical Explanation

Footnotes

- ¹⁾ With assembly of suitable MKP capacitor per terminal
²⁾ The specified maximum operation junction temperature T_{vjop} is 150°C

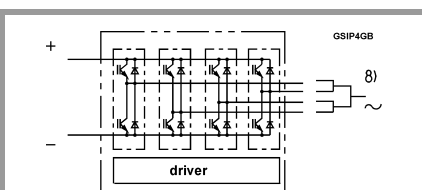
| Absolute Maximum Ratings | | | |
|--------------------------|--|------------|-----------------------|
| Symbol | Conditions | Values | Unit |
| System | | | |
| $V_{CC}^{1)}$ | Operating DC link voltage | 900 | V |
| V_{isol} | DC, $t = 1\text{ s}$, each polarity | 4300 | V |
| $I_{t(RMS)}$ | per AC terminal, rms, sinusoidal current | 500 | A |
| $I_{max (peak)}$ | max. peak current of power section | 3600 | A |
| I_{FSM} | $T_j = 175^{\circ}\text{C}$, $t_p = 10\text{ ms}$, sin 180° | 15885 | A |
| I^2t | $T_j = 175^{\circ}\text{C}$, $t_p = 10\text{ ms}$, diode | 1262 | kA^2s |
| f_{out} | fundamental output frequency (sinusoidal) | 1 | kHz |
| T_{stg} | storage temperature | -40 ... 85 | $^{\circ}\text{C}$ |

| IGBT | | | | |
|------------|-----------------------------|----------------------------|--------------------|---|
| V_{CES} | $T_j = 25^{\circ}\text{C}$ | 1200 | V | |
| I_C | $T_j = 175^{\circ}\text{C}$ | $T_s = 25^{\circ}\text{C}$ | 3109 | A |
| | | $T_s = 70^{\circ}\text{C}$ | 2528 | A |
| I_{Cnom} | | 2400 | A | |
| $T_j^{2)}$ | junction temperature | -40 ... 175 | $^{\circ}\text{C}$ | |

| Diode | | | | |
|------------|-----------------------------|----------------------------|--------------------|---|
| V_{RRM} | $T_j = 25^{\circ}\text{C}$ | 1200 | V | |
| I_F | $T_j = 175^{\circ}\text{C}$ | $T_s = 25^{\circ}\text{C}$ | 2369 | A |
| | | $T_s = 70^{\circ}\text{C}$ | 1878 | A |
| I_{Fnom} | | 2400 | A | |
| $T_j^{2)}$ | junction temperature | -40 ... 175 | $^{\circ}\text{C}$ | |

| Driver | | | |
|----------|-----------------------------|---------------|-------------------------|
| V_s | power supply | 19.2 ... 28.8 | V |
| V_{iH} | input signal voltage (high) | $V_s + 0.3$ | V |
| dv/dt | secondary to primary side | 75 | $\text{kV}/\mu\text{s}$ |
| f_{sw} | switching frequency | 10 | kHz |

| Characteristics | | | | | |
|--------------------|--|-----------------------------|------|--------|------------------|
| Symbol | Conditions | min. | typ. | max. | Unit |
| IGBT | | | | | |
| $V_{CE(sat)}$ | $I_C = 2400\text{ A}$ at terminal | $T_j = 25^{\circ}\text{C}$ | 2.01 | 2.26 | V |
| | | $T_j = 150^{\circ}\text{C}$ | 2.49 | 2.69 | V |
| V_{CE0} | | $T_j = 25^{\circ}\text{C}$ | 0.80 | 0.90 | V |
| | | $T_j = 150^{\circ}\text{C}$ | 0.70 | 0.80 | V |
| r_{CE} | at terminal | $T_j = 25^{\circ}\text{C}$ | 0.51 | 0.57 | $\text{m}\Omega$ |
| | | $T_j = 150^{\circ}\text{C}$ | 0.75 | 0.79 | $\text{m}\Omega$ |
| $E_{on} + E_{off}$ | $I_C = 2400\text{ A}$ $T_j = 150^{\circ}\text{C}$ | $V_{CC} = 600\text{ V}$ | 936 | | mJ |
| | | $V_{CC} = 900\text{ V}$ | 1680 | | mJ |
| $R_{th(j-s)}$ | per IGBT switch | | | 0.0159 | K/W |
| $R_{th(j-r)}$ | per IGBT switch | | | 0.0092 | K/W |



S44

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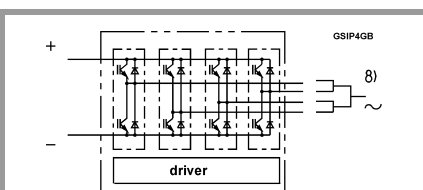
Remarks

For further information please refer to SKiiP®4 Technical Explanation

Footnotes

- 1) With assembly of suitable MKP capacitor per terminal
- 2) The specified maximum operation junction temperature T_{vjop} is 150°C

| Characteristics | | | | | | |
|--------------------------|--|-------------------------|--------------------|------------------------------------|------------------------------------|-------------------|
| Symbol | Conditions | | min. | typ. | max. | Unit |
| Diode | | | | | | |
| $V_F = V_{EC}$ | I _F = 2400 A at terminal | T _j = 25 °C | | 2.33 | 2.65 | V |
| | | T _j = 150 °C | | 2.35 | 2.66 | V |
| V _{F0} | | T _j = 25 °C | | 1.30 | 1.50 | V |
| | | T _j = 150 °C | | 0.90 | 1.10 | V |
| r _F | at terminal | T _j = 25 °C | | 0.43 | 0.48 | mΩ |
| | | T _j = 150 °C | | 0.61 | 0.65 | mΩ |
| E _{rr} | I _F = 2400 A T _j = 150 °C | V _R = 600 V | | 159 | | mJ |
| | | V _R = 900 V | | 200 | | mJ |
| R _{th(j-s)} | per diode switch | | | | 0.0281 | K/W |
| R _{th(j-r)} | per diode switch | | | | 0.02 | K/W |
| Driver | | | | | | |
| V _s | supply voltage non stabilized | | 19.2 | 24 | 28.8 | V |
| I _{SO} | bias current @V _s = 24V, f _{sw} = 0, I _{AC} = 0 | | | 360 | | mA |
| I _s | k ₁ = 33 mA/kHz, k ₂ = 0.258 mA/A, f _{out} = 50Hz, sinusoidal current | | = 360 | + k ₁ * f _{sw} | + k ₂ * I _{AC} | mA |
| V _{IT+} | input threshold voltage (HIGH) | | 0,7*V _s | | | V |
| V _{IT-} | input threshold voltage (LOW) | | | | 0,3*V _s | V |
| R _{IN} | input resistance | | | 13 | | kΩ |
| C _{IN} | input capacitance | | | 1 | | nF |
| t _{pRESET} | error memory reset time | | 1300 | | 2900 | ms |
| t _{pReset(OCP)} | Over current reset time | | | | | μs |
| t _{TD} | top / bottom switch interlock time | | | 3 | | μs |
| t _{jitter} | jitter clock time | | | 52 | 58 | ns |
| t _{SIS} | short pulse suppression time | | | 0.6 | | μs |
| t _{POR} | Power-On-Reset completed | | | 3.5 | | s |
| V _{CEstat} | Collector-Emitter Threshold Static Monitoring Voltage | | | 7.5 | | V |
| t _{bl} | Collector-Emitter Threshold Static Monitoring Blanking Time | | | 6 | | μs |
| I _{digiout} | digital output sink current (HALT-signal) | | | | 16 | mA |
| V _{it+ HALT} | input threshold voltage HIGH HALT (Low -->High) | | 0,6*V _s | | | V |
| V _{it- HALT} | input threshold voltage LOW HALT (High --> Low) | | | | 0.4*V _s | V |
| t _{d(Err)} | Error delay time (from detection to HALT), (depends on kind of error) | | 1.8 | | 170 | μs |
| I _{TRIPSC} | over current trip level | | 3525 | 3600 | 3675 | A _{PEAK} |
| T _{trip} | over temperature trip level | | 128 | 135 | 142 | °C |
| T _{DriverTrip} | over temperature PCB trip level | | 113 | 120 | 124 | °C |
| V _{DCtrip} | over voltage trip level, | | 950 | 980 | 1010 | V |



S44

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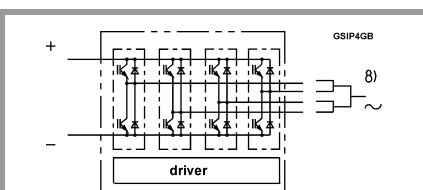
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| Characteristics | | | | | |
|--------------------|--|------|--------|--------|-------------------------|
| Symbol | Conditions | min. | typ. | max. | Unit |
| System | | | | | |
| $t_{d(on)IO}$ | $V_{CC} = 600\text{ V}$ $I_C = 2400\text{ A}$ | | 2.8 | | μs |
| $t_{d(off)IO}$ | $T_j = 25^{\circ}\text{C}$ | | 2.6 | | μs |
| dV_{CE}/dt_{on} | $T_j = 25^{\circ}\text{C}$ $V_{CC} = 600\text{ V}$ | | 9 | | $\text{kV}/\mu\text{s}$ |
| | | | | | $\text{kV}/\mu\text{s}$ |
| dV_{CE}/dt_{off} | | | 3 | | $\text{kV}/\mu\text{s}$ |
| $R_{th(s-a)}$ | flow rate = $550\text{ m}^3/\text{h}$, $T_a=25^{\circ}\text{C}$, 500m above sea level | | | 0.0225 | K/W |
| $R_{CC'+EE'}$ | terminals to chip, $T_s = 25^{\circ}\text{C}$ | | 0.0675 | | $\text{m}\Omega$ |
| L_{CE} | commutation inductance | | 4.5 | | nH |
| C_{CHC} | coupling capacitance secondary to heat sink | | 6 | | nF |
| C_{ps} | coupling capacitance primary to secondary | | 0.08 | | nF |
| $I_{CES} + I_{RD}$ | $V_{GE} = 0\text{ V}$, $V_{CE} = 1200\text{ V}$, $T_j = 25^{\circ}\text{C}$ | | 0.209 | | mA |
| M_{dc} | DC terminals | 6 | | 8 | Nm |
| M_{ac} | AC terminals | 13 | | 15 | Nm |
| w | SKiiP System w/o heat sink | | 3.22 | | kg |
| w_h | heat sink | | 7.55 | | kg |



S44

| | |
|---|---------------------------------------|
| Isolation coordination acc. to EN 50178 and IEC 61800-5-1 | |
| Maximum grid RMS voltage, line-to-line, grounded delta mains | 480V+20% |
| Installation altitude for maximum grid RMS voltage, line-to-line, grounded delta mains | 4000m |
| Maximum grid RMS voltage, line-to-line, star point grounded mains | 480V+20% |
| Installation altitude for maximum grid RMS voltage, line-to-line, star point grounded mains | 8000m |
| Maximum transient peak voltage between low voltage circuit and mains | 1900V |
| Pollution degree acc. to IEC 60664-1 outside the moulded power section | 2 |
| Overvoltage cat. acc. to IEC 60664-1 for mains | III |
| Overvoltage cat. acc. to UL 840 within mains | I |
| Overvoltage cat. acc. to UL 840 between mains and ground | III |
| Overvoltage cat. acc. to UL 840 between mains and low voltage circuit | III |
| Basic isolation | between heat sink and mains |
| Reinforced isolation | between low voltage circuit and mains |
| Protection level acc. to IEC 60529 | IP00 |

Environmental conditions acc. to IEC 60721

| | Storage | Transportation | Operation stationary use at weather protected locations | Operating ground vehicle installations | Operating ship environment |
|--|--------------------|--------------------|---|--|----------------------------|
| Climatic conditions | 1K2 ₍₁₎ | 2K2 ₍₁₎ | 3K3 ₍₁₎ | 5K1 ₍₁₎ | 6K1 ₍₁₎ |
| Biological conditions | 1B1 | 2B1 | 3B1 | 5B1 | 6B1 |
| Chemically active substances (excluded: salt spray) | 1C2 | 2C1 | 3C2 | 5C2 | 6C2 |
| Mechanically active substances | 1S1 | 2S1 | 3S1 | 5S1 | 6S1 |
| Mechanical conditions | 1M3 | (4) | 3M6 ₍₂₎ | 5M3 ₍₃₎ | 6M3 |
| Contaminating fluids | --- | --- | --- | 5F1 | --- |

(1) expanded temperature range: -40°C / +85°C. Please note: by operation near 85°C the life time of product is reduced.

(2) 3M7 possible, but due to the mechanic load capacity of external components like DC-Link capacitors limited to 3M6

(3) 5M3 without impact of foreign bodies, stones

(4) no declaration due to customer-specific packing

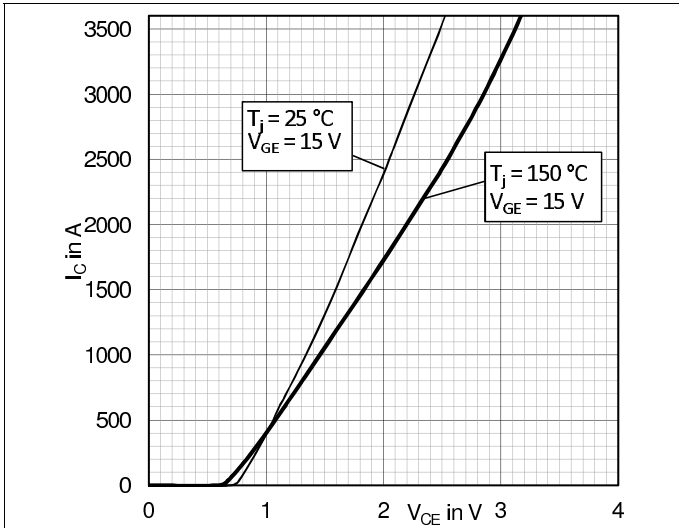


Fig. 1: Typical IGBT output characteristics

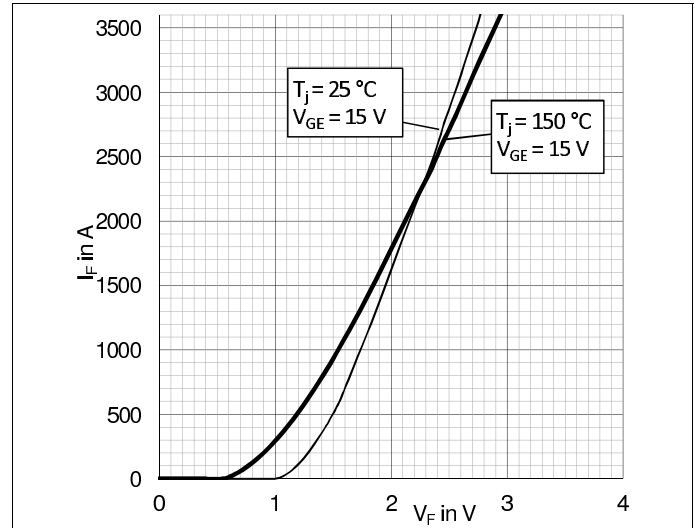


Fig. 2: Typical diode output characteristics

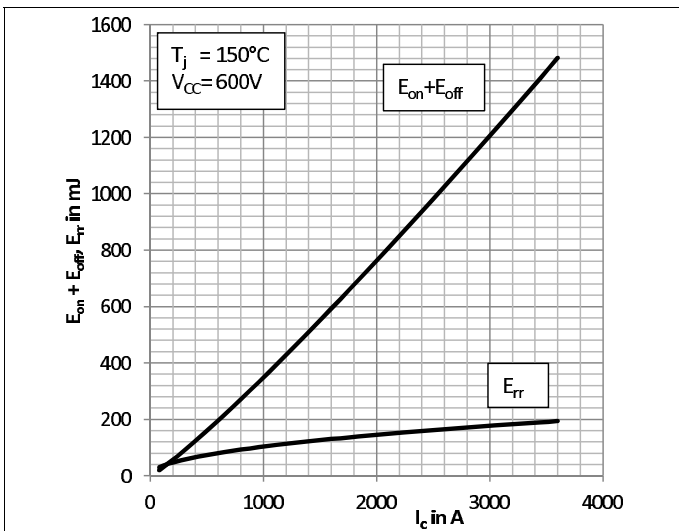


Fig. 3: Typical switching energy $E = f(I_C)$

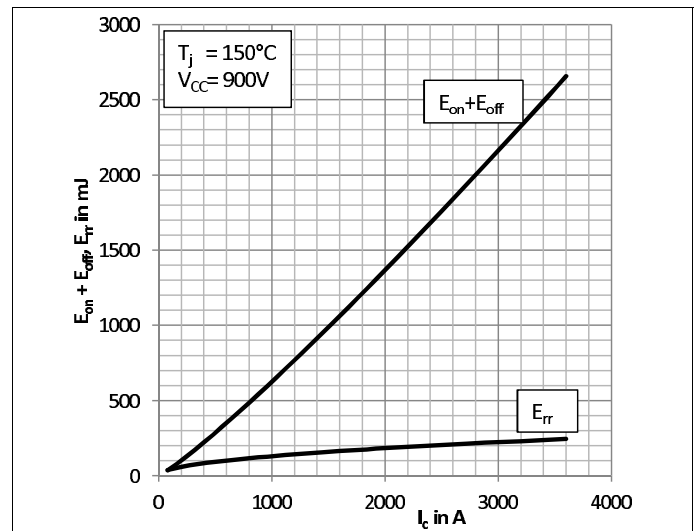


Fig. 4: Typical switching energy $E = f(I_C)$

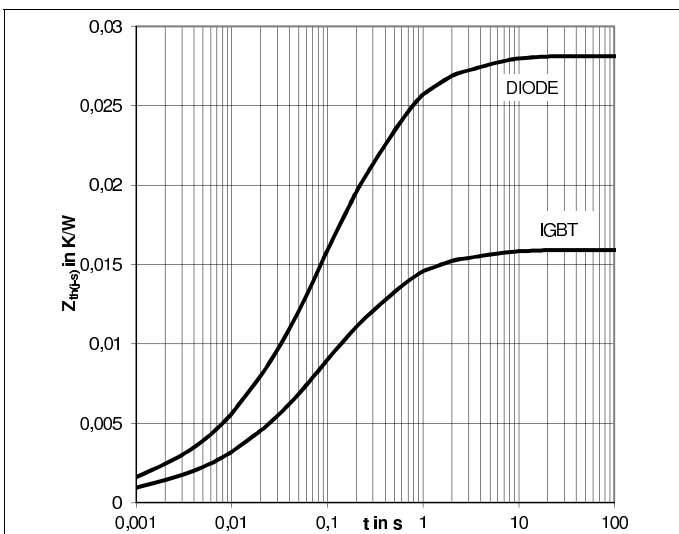


Fig. 5: Transient thermal impedance $Z_{th}(j-s)$

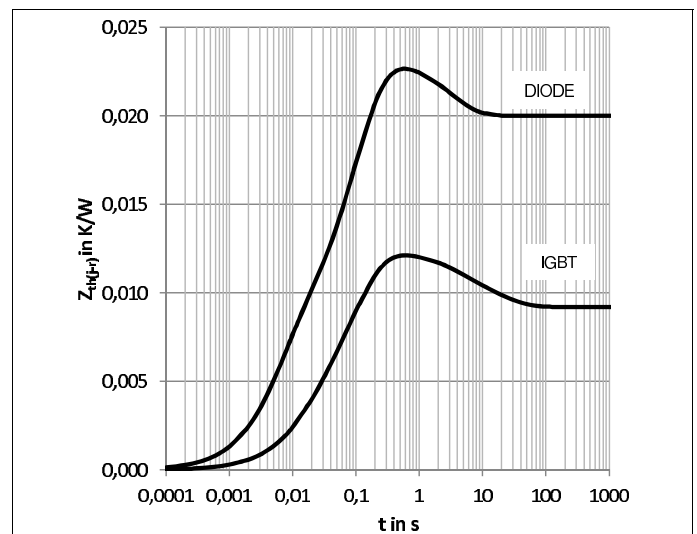


Fig. 6: Transient thermal impedance $Z_{th}(j-r)$

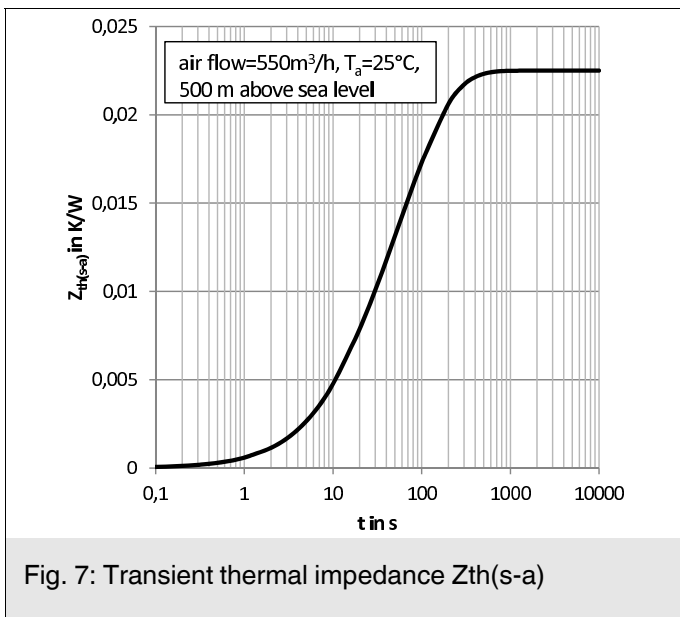


Fig. 7: Transient thermal impedance Z_{th}(s-a)

| | R _{th} [K/W] | | | | |
|------------------------|-----------------------|--------|--------|---------|---------|
| | 1 | 2 | 3 | 4 | 5 |
| Z _{th(j-s)} I | 0,0010 | 0,0049 | 0,0055 | 0,0017 | 0,0007 |
| Z _{th(j-s)} D | 0,0020 | 0,0100 | 0,0112 | 0,0034 | 0,0015 |
| Z _{th(j-r)} I | 0,0024 | 0,0033 | 0,0067 | -0,0015 | -0,0017 |
| Z _{th(j-r)} D | 0,0075 | 0,0060 | 0,0098 | -0,0033 | 0,0000 |
| Z _{th(s-a)} | 0,0012 | 0,0052 | 0,0123 | 0,0038 | 0,0000 |
| | tau [s] | | | | |
| | 1 | 2 | 3 | 4 | 5 |
| Z _{th(j-s)} I | 3,6500 | 0,4100 | 0,0650 | 0,0090 | 0,0008 |
| Z _{th(j-s)} D | 3,6500 | 0,4100 | 0,0650 | 0,0090 | 0,0008 |
| Z _{th(j-r)} I | 0,0130 | 0,0500 | 0,1200 | 4,4000 | 21,000 |
| Z _{th(j-r)} D | 0,0060 | 0,0650 | 0,1300 | 3,2500 | 1,0000 |
| Z _{th(s-a)} | 9,0000 | 18,900 | 73,000 | 161,00 | 0,0000 |

Fig. 8: Coefficients of thermal impedances

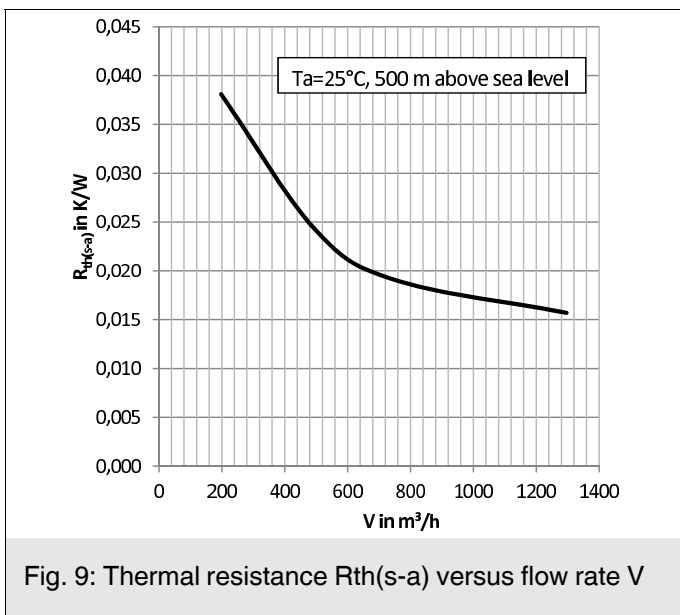


Fig. 9: Thermal resistance R_{th}(s-a) versus flow rate V

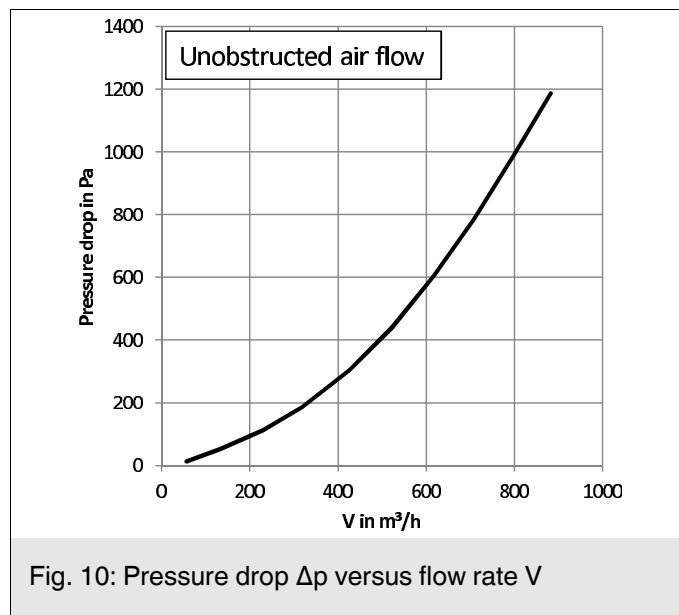
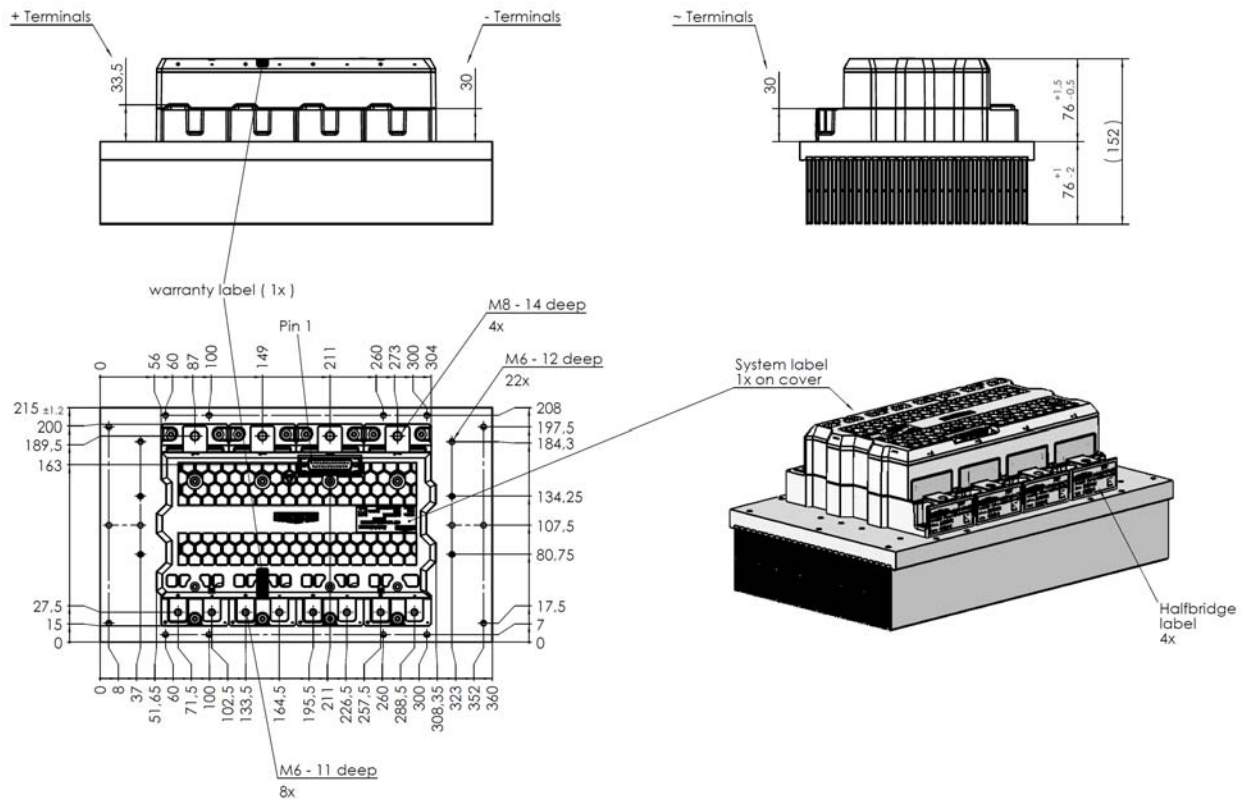


Fig. 10: Pressure drop Δp versus flow rate V

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This is an electrostatic discharge sensitive device (ESDS), international standard IEC 60747-1, Chapter IX

* The specifications of our components may not be considered as an assurance of component characteristics. Components have to be tested for the respective application. Adjustments may be necessary. The use of SEMIKRON products in life support appliances and systems is subject to prior specification and written approval by SEMIKRON. We therefore strongly recommend prior consultation of our staff.